

PATENT

10/716,159

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Junichi Kishida  
Serial No.: 10/716,159  
Filing Date: November 18, 2003  
For: BOOTABLE NAND FLASH MEMORY ARCHITECTURE  
Examiner: Behzad Peikari  
Art Unit: 2189  
Confirmation No: 5285

SUBMISSION OF FORMAL DRAWINGS

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

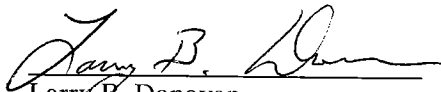
Enclosed for filing in the above-identified application are the required 5 sheets of formal drawings.

The drawing informalities indicated on any notices regarding the patent drawings, if any, have been corrected.

The Commissioner is hereby authorized to charge any fees associated with this communication to Deposit Account No. 50-0902 (64272/00001).

Respectfully submitted,

Date: January 30, 2007



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